



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No. K

Indian application of

Shigeyuki UEDA

Serial No. 09/665,663

Filed: September 20, 2000

Title:

Examiner: W. COLEMAN

SEMICONDUCTOR CHIP AND METHOD OF PRODUCING THE SAME

Group Art Unit: 2828 282

## RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents Washington, D.C. 20231

Sir:

In response to the Office Action dated November 8, 2001, Applicant hereby elects Group I claims 1-7.

Applicants, of course, reserve the right to file a divisional application covering the subject matter of the non-elected group.

Receipt of the initial Office Action on the merits is awaited.

In the event that any additional fees are required with respect to this paper, the Commissioner is hereby authorized to charge our Deposit Account No.18-0013.

Respectfully submitted,

14/7/2001 Date:

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Should additional fees be necessary in connection with the filing of this paper, or if a petition for extension of time is required for timely acceptance of same, the Commissioner is hereby authorized to charge Deposit Account No. 180013 for any such fees; and applicant(s) hereby petition for any needed extension of time.